

FIG.2

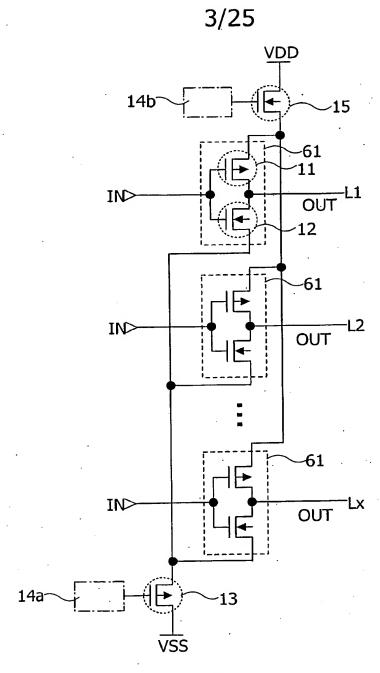
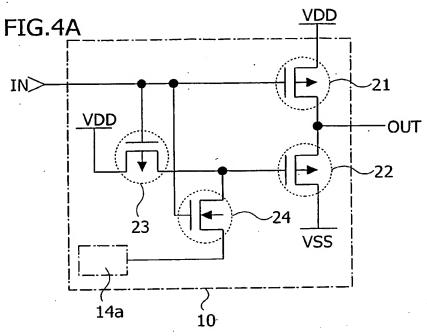
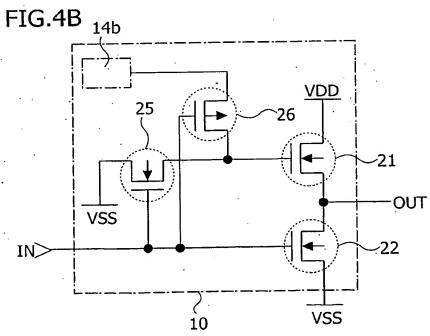
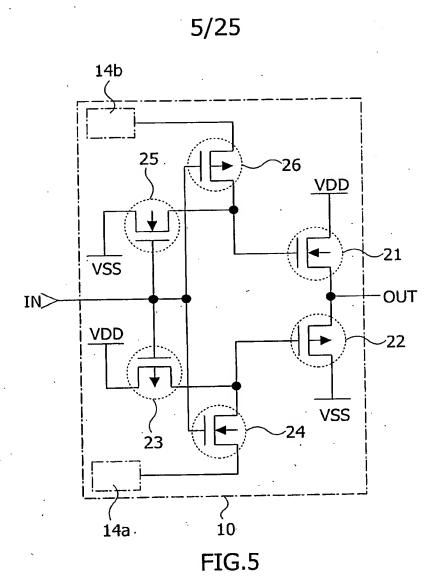


FIG.3

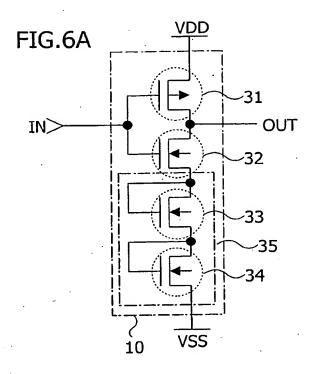
4/25

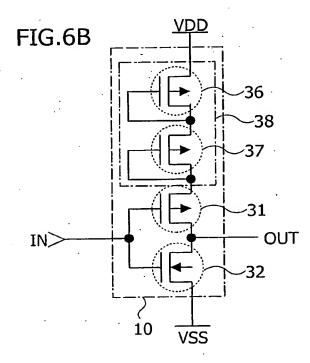






6/25





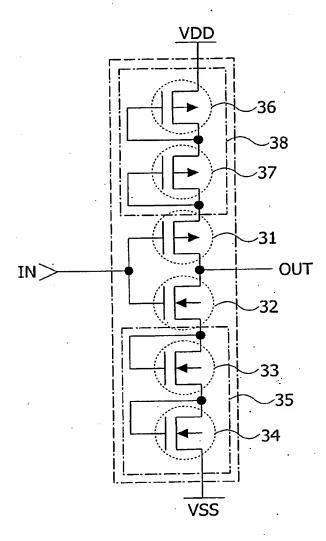


FIG.7

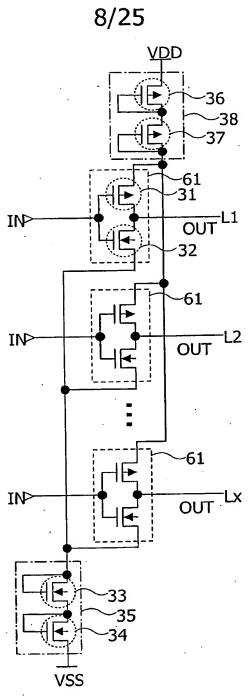


FIG.8

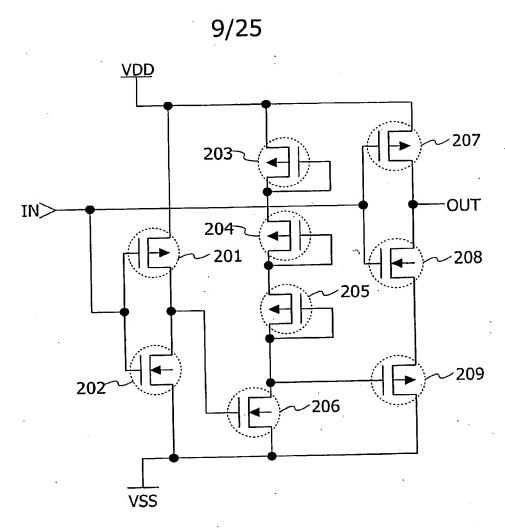


FIG.9

VSS

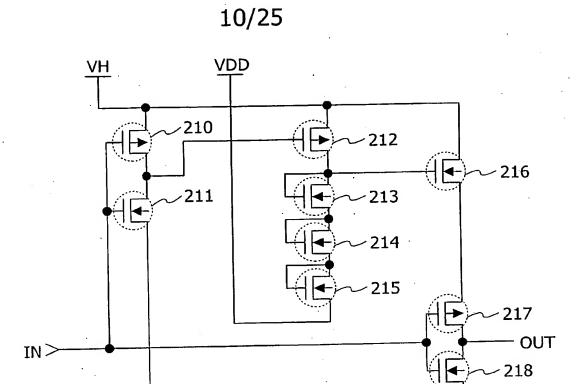


FIG.10

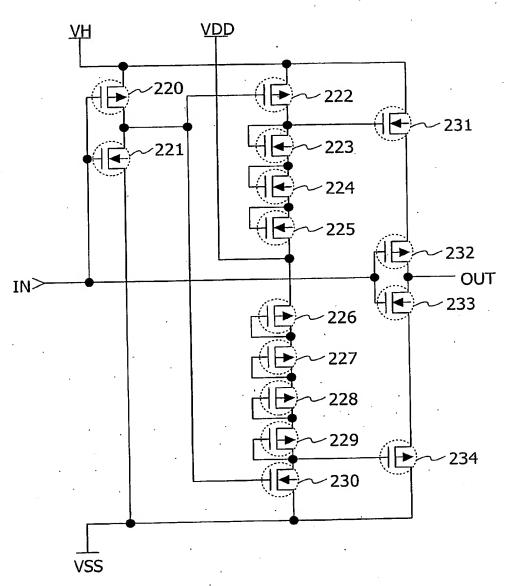


FIG.11

12/25

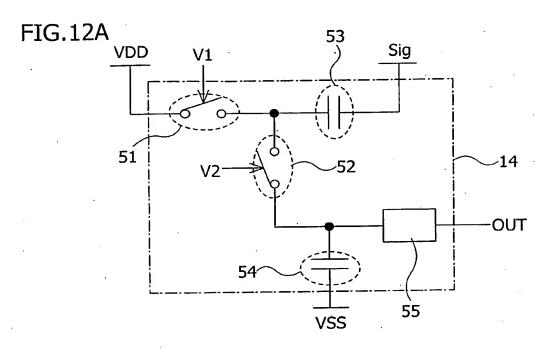
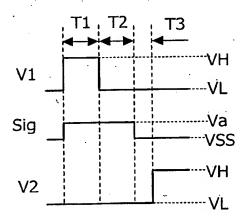


FIG.12B



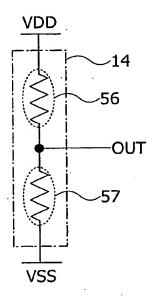


FIG.13

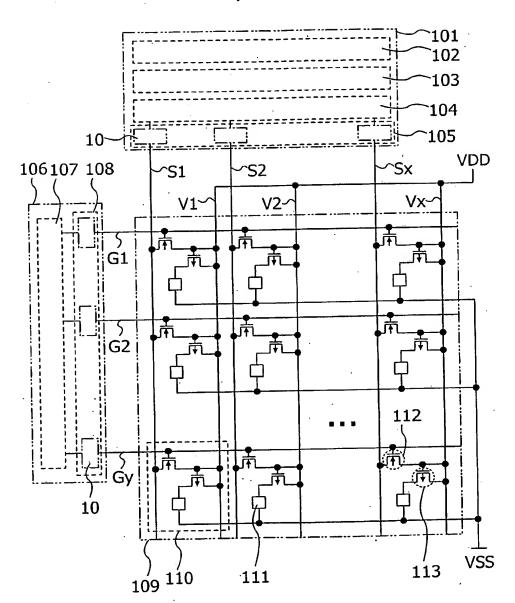


FIG.14

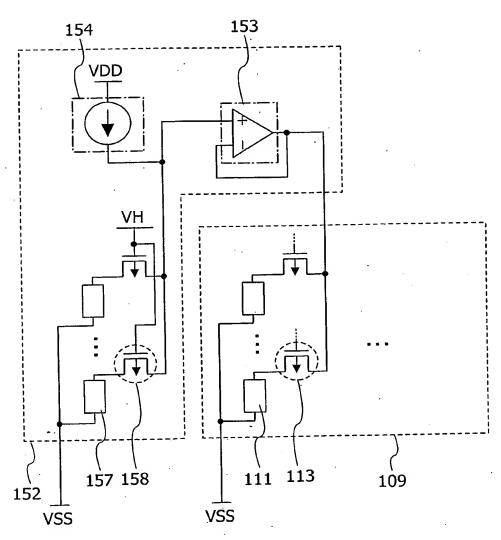
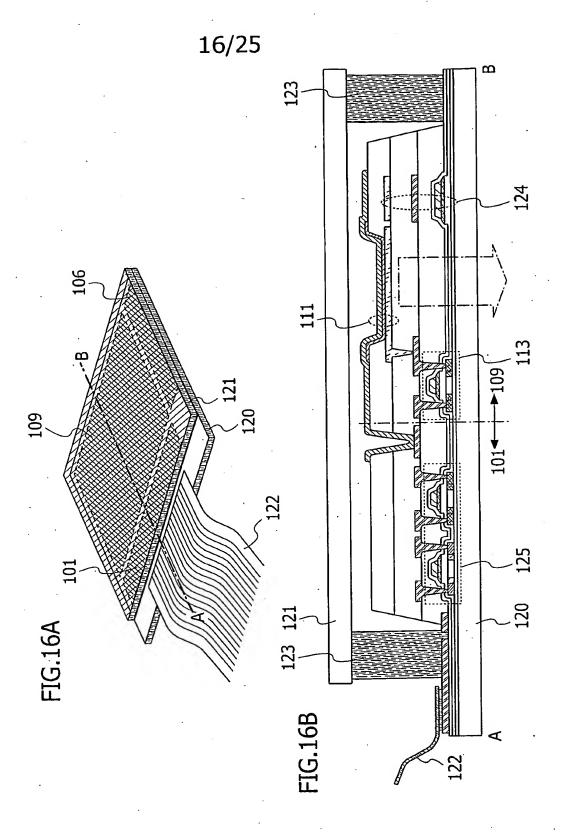
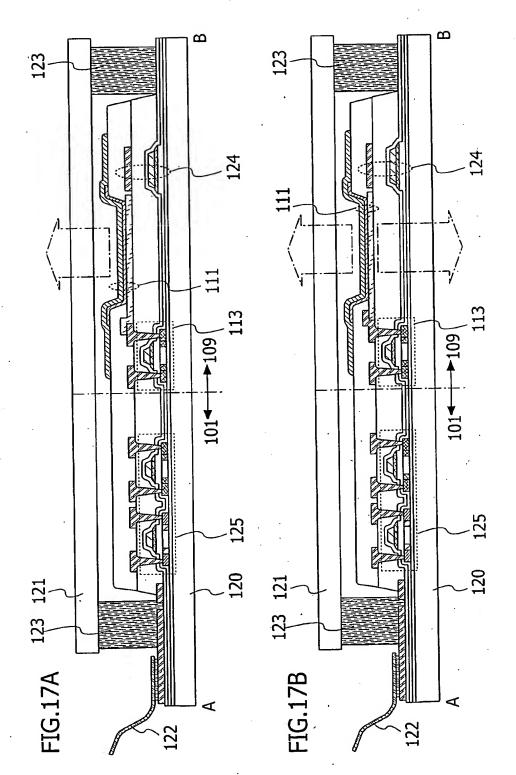


FIG.15



17/25



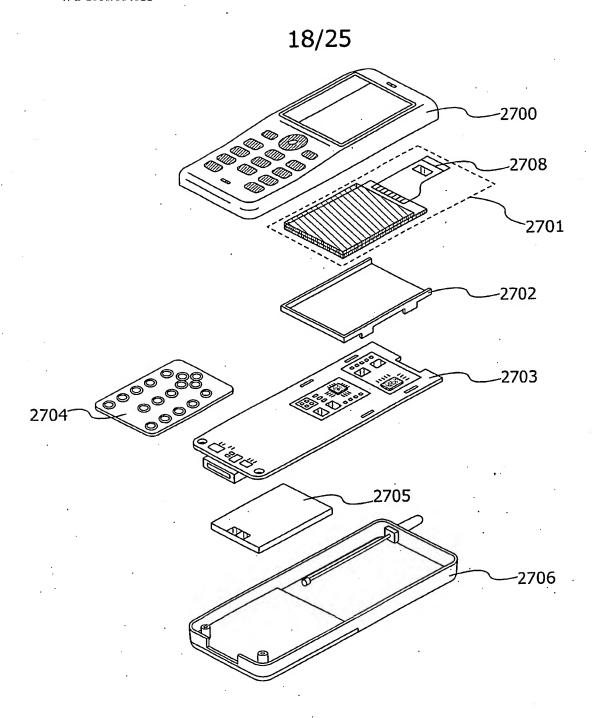
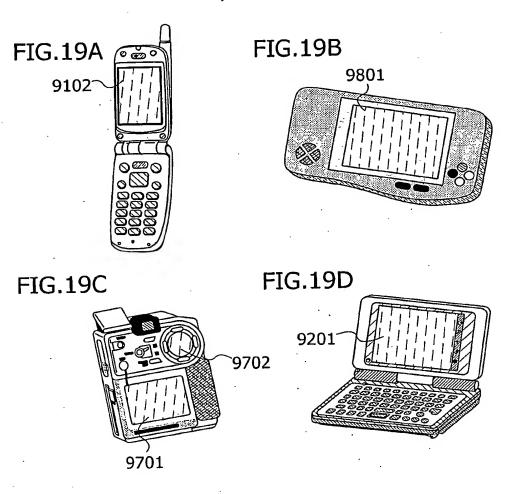
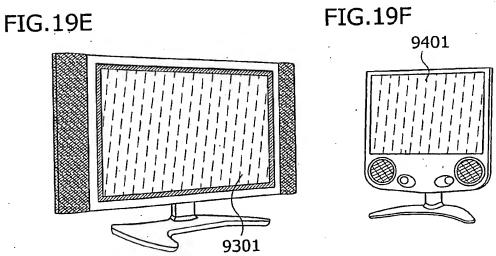


FIG.18

19/25





20/25

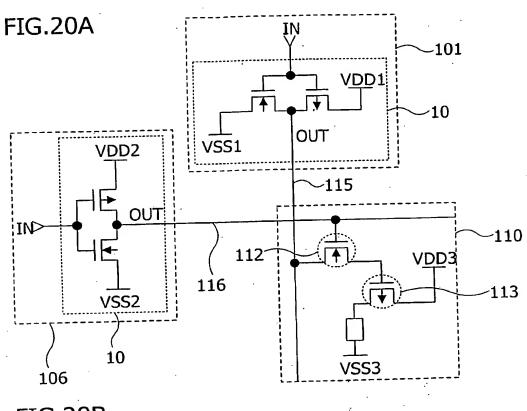


FIG.20B

VDD1 OR VDD2

VSS1 OR VSS2

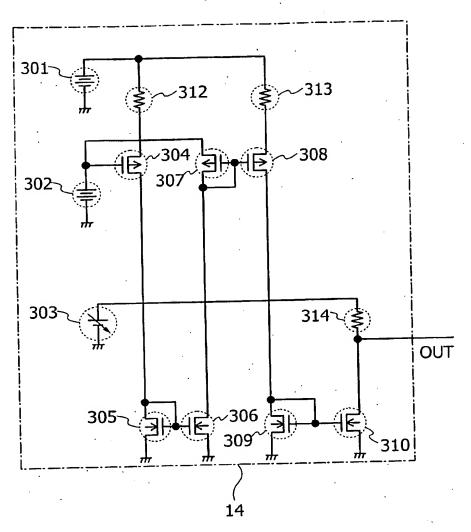


FIG.21

22/25

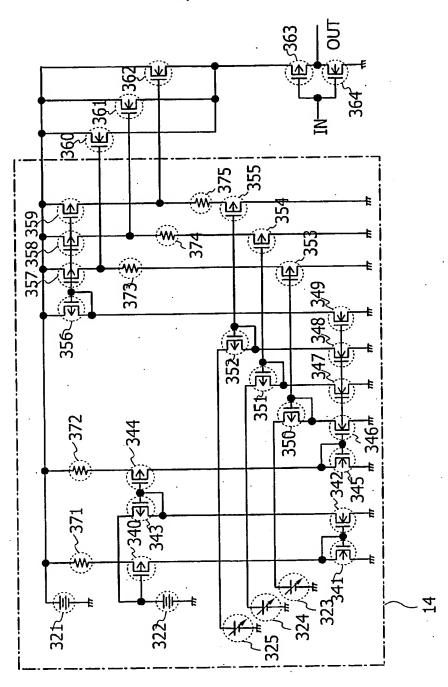


FIG.22

23/25

